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Patents

Patents **1 - 10** on **first mode with counter mode**. (**0.16** seconds)

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Information processor executing interruption program without saving contents ...
US Pat. 5036458 - Filed Dec 20, 1988 - NEC Corporation
... memory for saving a content of at least one of said program **counter** and ...
said **first mode** signal, for saving the contents of said program **counter** and ...

Image forming apparatus which modifies image forming condition depending on ...
US Pat. 6061542 - Filed Aug 20, 1998 - Minolta Co., Ltd.
15 comprising a **counter** for counting a number of image 40. ... unit further
includes a **counter**, the **counter** count- with transporting unit in the **first mode**, ...

GSM cellular telephone and GPS receiver combination
US Pat. 6122506 - Filed May 4, 1998 - Trimble Navigation Limited
The divide select signal provided by the **counter** 68 causes the divider 66 to
divide by thirteen or by fourteen. In the **first mode** the **counter** 68 counts the ...

Total sales indication device for a vending machine
US Pat. 4376479 - Filed Oct 14, 1980 - Kabushiki Kaisha Nippon Coinco
If the vend test switch 30 is depressed once again during this **first mode**, the
ternary **counter** 29 shifted from the step "2" to the step "0. ...

Communication multiplexer having a variable priority scheme using a read ...
US Pat. 4385382 - Filed Sep 29, 1980 - Honeywell Information Systems Inc.
The system of claim 1 wherein said **first counter** means comprises: a **first counter**
for initially generating said plurality of **mode** signals indicative of said ...

FRAME SYNCHRONIZATION SYSTEM
US Pat. 3798378 - Filed Nov 7, 1972 - International Telephone and Telegraph Corporation
During the **first mode** of operation the **first** output signal is coupled to the
decision **counter** and when the **first** control signal is below or equal to the ...

Anti-skid brake control system with braking force quick take-up feature
US Pat. 4748564 - Filed May 14, 1985 - Nissan Motor Company, Limited
The **counter** 261 **first mode** is selected when said vehicular braking value by 1 in
response to each leading edge in the output **first mode** for increasing said ...

Electronic thermometer and associated apparatus
US Pat. 4536851 - Filed Oct 22, 1982
... said clock **counter** and a second input selectively coupled to said **first** and
... and means for selecting said **first** storage means during said **first mode** ...

Method and system for performance per-thread monitoring in a multithreaded ...
US Pat. 5835705 - Filed Mar 11, 1997 - International Business Machines Corporation

The system of claim 1, said performance monitor including a **first counter** and a second **counter** and having a **first mode** of operation, wherein when said ...

Vehicle repeater prioritization system

US Pat. 4056780 - Filed Jun 25, 1975 - Motorola, Inc.

... 5 c. activating the **counter** to a high count output in a **first mode** and an ...

c. activating the **counter** to its **first mode** 'if the gate is in its **first** ...

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Patents

Patents **1 - 10** on **second mode with cipher block chaining mode**. (**0.23** seconds)

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Block cipher mode of operation for secure, length-preserving encryption
US Pat. 5673319 - Filed Feb 6, 1995 - International Business Machines Corporation
A method, using first and **second** keys and a **block cipher**, ... (a) decrypting by
cipher block chaining the enciphered 35 string using the **second** key and the ...

Secure communication system with cross linked cryptographic codes
US Pat. 5671283 - Filed Jun 8, 1995 - Wave Systems Corp.
... said first and **second** terminals, said method comprising: encrypting said
message under a first cryptographic code using a **cipher block chaining mode** to ...

Secure processor with external memory using block chaining and block re-ordering
US Pat. 6061449 - Filed Oct 10, 1997 - General Instrument Corporation
With **cipher block chaining**, changing one **block** of data can change subsequent ...
to the outside world in a clear **mode**, eg, for printers, error messages, ...

Cipher block chaining mode in encryption/decryption processing
US Pat. 7006627 - Filed Sep 28, 2001 - Tarari, Inc.
More particularly, the present invention pertains to **cipher block chaining mode**
in the Data Encryption Standard (DBS). 10 There are a variety of encryption ...

System for translating encrypted data
US Pat. 5381480 - Filed Sep 20, 1993 - International Business Machines Corporation
... forms the **second** intermediate value At site 11, this **second cipher block** output
... for decryption of the (ECB) **mode**, **cipher block chaining** (CBC) **mode**, ...

Muti-cycle non-parallel data encryption engine
US Pat. 5835599 - Filed Apr 15, 1996 - VLSI Technology, Inc.
... wherein the **cipher** stages correspond to a **cipher block chaining mode** of ...
f) generating a **second** encrypted/decrypted **block** of data by a **second cipher** ...

Autotest of encryption algorithms in embedded secure encryption devices
US Pat. 5631960 - Filed Aug 31, 1995 - National Semiconductor Corporation
... wherein the first **mode** is Output Feedback **Mode**; 5 wherein the **second mode** is
Electronic Codebook **Mode**; wherein the third **mode** is **Cipher Block Chaining** ...

Method and apparatus for data encryption or decryption
US Pat. 5195136 - Filed Sep 30, 1991 - Motorola, Inc.
These techniques are the electronic code 25 «ty of error extension makes it very
difficult to use book **mode**, the **cipher block chaining mode**, and the svstems ...

Cryptographic system with masking
US Pat. 5768390 - Filed Jul 17, 1997 - International Business Machines Corporation

Second, it is backwards 08/. filed Oct. 25, 1995, now abandoned. value for ...
key book (ECB) **mode** or **cipher block chaining** (CBC) **mode**. plaintext blocks, ...

Data security system and method
US Pat. 6028939 - Filed Jan 3, 1997 - RedCreek Communications, Inc.
10 DBS is a **block cipher** system which operates on blocks of 64 bits of data in
... operation in a **second** cryptographic **mode** known as **cipher block chaining** ...

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Patents Patents 1 - 10 on **first mode with counter mode and second mode with cipher block chaining mode**. (0.11 secoı

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Block cipher mode of operation for secure, length-preserving encryption
US Pat. 5673319 - Filed Feb 6, 1995 - International Business Machines Corporation
The method begins by with the last **block** of the ciphertext depending on all of
the **cipher block chaining** the plaintext string using the **first** key well-known ...

Autotest of encryption algorithms in embedded secure encryption devices
US Pat. 5631960 - Filed Aug 31, 1995 - National Semiconductor Corporation
The **second** uses Electronic **mode** is 1011. At step 408, illustrated in FIG. 5F, a
Cipher Codebook ECB **mode**, the third **Cipher Block Chaining 5 Block chaining** ...

Cipher block chaining decryption
US Pat. 7200226 - Filed Sep 4, 2003 - Intel Corporation
If the current ciphertext **block** (C,) is the **first block** require the ... with the
where "j" is a **counter**, and "M" is the number of blocks in 25 acting as the ...

Method for statistics mode reloading and for statistical acquisition ...
US Pat. 6148292 - Filed Jul 14, 1998 - Francotyp-Postalia AG & Co..
One shipping **mode** to be separately accounted for is, for example, **"first ...**
data are stored CBC **mode** (data encryption standard & **cipher block** 55 in a ...

Method for establishing session key agreement
US Pat. 6591364 - Filed Aug 28, 1998 - Lucent Technologies Inc.
Preferably, the KCF is a keyed message authentication code such as HMAC, but
could be a PRF such as Data Encryption Standard-**Cipher Block Chaining** (DES- ...

Transaction security system
US Pat. 4755940 - Filed Jan 6, 1987 - International Business Machines Corporation
The method used is the standard **cipher block chaining** (CBC) **mode** of DBS. ...
The **second, block** X2 is then XOR'd with the result of the **first** encipherment ...

Information processing apparatus with a software protecting function
US Pat. 5995623 - Filed Jan 27, 1997 - Fuji Xerox Co., Ltd.
A third example is the method of raising the level of the encryption security by
utilizing the **Cipher Block Chaining** (CBC) or the **Cipher** Feed Back (CFB), ...

System and method for communications with postage meters
US Pat. 5715164 - Filed Dec 14, 1994 - Ascom Hasler Mailing Systems AG
2 is a **block** diagram of a postage meter of FIG. 1; CBC **mode** encrypts each **block**
... which is a function ^^ 3A ^(rates * memory map of memory space **chaining**, ...

Part determination device, part attachment and detachment apparatus, image ...

US Pat. 7177553 - Filed Jan 11, 2005 - Fuji Xerox Co., Ltd.
... also be an initial vector or the like used with CBC (**Cipher Block Chaining**)
mode, etc. ... In the above-described **first** and **second** specific examples, ...

High-speed data communications modem
US Pat. 5787483 - Filed Sep 22, 1995 - Hewlett-Packard Company
The reason for this arrangement is the fact that a **cipher block** can be one of
several types ... IV register, and sets (he **mode** register of the **cipher** chip ...

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